



RAJIV GANDHI COLLEGE OF ENGINEERING & RESEARCH, NAGPUR

Wanadongari, Hingna Road, Nagpur-441110

Department of Electronics & Telecommunication Engineering

TIMETABLE SESSION 2018-19 :- III SEMI ETC (A)



MESHE GROUP
VALUES REDEFINED

Class Room No.: R-222

w.e.f 13/08/2018

DAY	10.15-11.15 am	11.15-12.15 pm	12.15-1.00 pm	1.00-2.00 pm	2.00-3.00 pm	3.00-3.15pm	3.15-4.15pm	4.15-5.15pm	
MON	M-III AAA	EDC PW	R E C E S S	NAS AM	OOP&DS PD	R E C E S S	EMI PA	M-III (B2-AAA)/ OOP&DS(B1-PD)	
TUE	EDC/III SEMI-A/ETC/PW/B1 EMI/III SEMI-A/ETC/PA/B2 OOP&DS/III SEMI-A/ETC/PD/B3			NAS AM	M-III (B1-AAA)/ OOP&DS(B2-PD)		EDC PW	M-III AAA	
WED	EEP			M-III AAA	EDC PW		NAS AM	EMI PA	
THU	EDC/III SEMI-A/ETC/PW/B2 EMI/III SEMI-A/ETC/PA/B3 OOP&DS/III SEMI-A/ETC/PD/B1			EMI PA	OOP&DS PD		NAS AM	OOP&DS PD	
FRI	EDC/III SEMI-A/ETC/PW/B3 EMI/III SEMI-A/ETC/PA/B1 OOP&DS/III SEMI-A/ETC/PD/B2			EMI PA	M-III AAA		EDC PW	NAS(B1-AM)/ EDC(B2-PW)	
SAT	NAS(B2-AM)/ EDC(B1-PW)	OOP&DS PD		NAS(B2-AM)/ EDC(B1-PW)	OOP&DS PD		HOBBY CLUB AG/SSK/PM		

Name of Subject	Initials	Name of Faculty
M-III APPLIED MATHEMATICS- III	AAA	Mrs. ANUSHREE ASERKAR
EDC ELECTRONIC DEVICES AND CIRCUITS	PW	Mr. PRASHANT WANJARI
EMI & I ELECTRONICS MEASUREMENT AND INSTRUMENTATION	PA	MS.POONAM AGARKAR
OOP & DS OBJECT ORIENTED PROGRAMMING & DATA STRUCTURE	PD	Ms. PARWANI DHOTE
NAS NETWORK ANALYSIS AND SYNTHESIS	AM	MRS.ANURADHA MUNSHI
HC HOBBY CLUB	AG	Ms. APURVA GANAR
	SSK	MRS. SHILPA KATRE
	PM	Ms.PRIYANKA MANDAVGADE

LAB	LAB NO
EDC	R-215
EMI & I	R-213
OOP&DS	R-228
HC	R-218

Class Incharge : Ms. Poonam Agarkar

Practical Batches	Tutorial Batches
B1(Roll No 1-13)	B1 (ROLL NO. 1-20)
B2(Roll No 14-26)	B2 (ROLL NO. 21- onwards)
B3(Roll No 27-37)	

Ms. POONAM AGARKAR
TIME TABLE INCHARGE

Dr. N. G. NAROLE
HOD ETC

Dr. P. DEBRE
DEAN ACADEMICS

Dr. M. KSHIRSAGAR
PRINCIPAL



RAJIV GANDHI COLLEGE OF ENGINEERING & RESEARCH, NAGPUR

Wanadongri, Hingana Road, Nagpur-441110

Department of Electronics & Telecommunication Engineering

TIMETABLE SESSION 2018-19 :- V SEM ETC (A)



Class Room No.: R-223

w.e.f 13/08/2018

DAY	10.15-11.15 am	11.15-12.15 pm	12.15-1.00pm	1.00-2.00 pm	2.00-3.00 pm	3.00-3.15pm	3.15-4.15pm	4.15-5.15pm
MON	EEP		R E C E S S	uP&uC /MDR/T1 CE/SM/T2 TU	ACD MPK	R E C E S S	AWP SG	ACD/MPK/T1 AWP/SG/T2 TU
TUE	IEED DB	ACD MPK		uP&uC /MDR/T2 CE/SM/T1 TU	CE SM		uP&uC/V SEM-A/ETC/MDR/B1 ACD/V SEM-A/ETC/MPK/B2 CE/V SEM-A/ETC/PA/B3	
WED	ACD MPK	uP&uC MDR		IEED DB	CE SM		AWP SG	uP&uC MDR
THU	uP&uC MDR	CE SM		AWP SG	IEED DB		uP&uC/V SEM-A/ETC/MDR/B2 ACD/V SEM-A/ETC/MPK/B3 CE/V SEM-A/ETC/PA/B1	
FRI	CE SM	AWP/SG/T1 ACD/MPK/T2 TU		ACD MPK	uP&uC MDR		uP&uC/V SEM-A/ETC/MDR/B3 ACD/V SEM-A/ETC/MPK/B1 CE/V SEM-A/ETC/PA/B2	
SAT	IEED DB	AWP SG		IEED DB	AWP SG		GATE FORUM ACTIVITY/SUDENT INTERACTION/LIBRARY	

Name of Subject		Initials	Name of Faculty
UP & UC	Microprocessor & Microcontroller	MDR	MS. MANISHA RAUT
CE	Communication Electronics	SM	MS.SWATI MOHOD
		PA	Ms. POONAM AGARKAR
ACD	Analog Circuits & Design	MPK	MRS. MANISHA KHORGADE
AWP	Antenna & Wave Propagation	SG	MS. SHREYA GOLE
IEED	Industrial Economics & Entrepreneurship Development	DB	Mr. DHIRAJ BHOJNE

LAB	LAB NO
UP & UC	R-229
CE	R-213
ACD	R-230

Class Incharge: MS. MANISHA RAUT

Practical Batches	B1(Roll No 1-24)	Tutorial Batches	B1(Roll No 1-35)
	B2(Roll No 25-48)		B2(Roll No 36-onwards)
	B3(Roll No 49-75)		

Ms. POONAM AGARKAR
TIME TABLE INCHARGE

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HOD ETC

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DEAN ACADEMICS

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PRINCIPAL



RAJIV GANDHI COLLEGE OF ENGINEERING & RESEARCH, NAGPUR

Wanadongri, Hingana Road Nagpur-441110

Department of Electronics & Telecommunication Engineering

TIMETABLE SESSION 2018-19 :- VII Sem ETC(A)



MEGHE GROUP
VALUES REDEFINED

Class Room No.: R- 224

w.e.f 13/08/2018

DAY	10.15-11.15 am	11.15-12.15 pm	12.15-1.00pm	1.00-2.00 pm	2.00-3.00 pm	3.00-3.15pm	3.15-4.15pm	4.15-5.15pm
MON	DSP&A NGN	OC YD(A)	R E C E S S	TVE KT(A)	ADSD SJ	R E C E S S	DSP P&A/VII SEM-A/ETC/DB/B1 ADSD/VII SEM-A/ETC/PDM/B2 TVE/VII SEM-A/ETC/KT/B3	
TUE	OC YD(A)	VSP/MEMS AG/PC		TVE KT(A)	ADSD SJ		DSP P&A/VII SEM-A/ETC/NGN/DB/B2 ADSD/VII SEM-A/ETC/SJ/B3 TVE/VII SEM-A/ETC/KT/B1	
WED	OC YD(A)	VSP/MEMS AG/PC		OC YD(A)	DSP&A NGN		PROJECT	
THU	ADSD SJ	VSP/MEMS AG/PC		TVE KT(A)	TVE KT(A)		ADSD/SJ/T2 DSP&A/NGN/T1	VSP/MEMS AG/PC(TU)
FRI	DSP&A NGN	ADSD/SJ/T1 DSP&A/NGN/T2		ADSD SJ	DSP&A NGN		ADSD/VII SEM-A/ETC/SJ/B1 DSP P&A/VII SEM-A/ETC/NGN/DB/B3 TVE/VII SEM-A/ETC/KT/B2	
SAT	TVE (TU) KT(A)	TVE (TU) KT(A)		PROJECT			GATE FORUM/SUDENT INTERACTION/LIBRARY	

Name of Subject	Initials	Name of Faculty
DSPP & A	NGN	DR. N.G. NAROLE
	DB	Mr. DHIRAJ BHOJNE
ADSD	SJ	Mr. SHASHANT JAYKAR
OC	YD	MR. YASHWANT DEODHE
TVE	KT	Ms. KOMAL TUMSARE
MEMS	PC	MS. POONAM CHOUDHARY
VSP	AG	Ms. APURVA GANAR

LAB	LAB NO
DSP&A	R-227
ADSD	R-228
TVE	R-216
PROJECT	R-228

Practical Batches	Tutorial Batches
B1(Roll No 1-15)	B1(Roll No 1-35)
B2(Roll No 15-30)	
B3(Roll No31-45)	
B4(Roll No 46-onwards)	B2(Roll No 36-onwards)

Class Incharge: MR. YASHWANT DEODHE

Ms. POONAM AGARKAR
TIME TABLE INCHARGE

Dr. N. G. NAROLE
HOD ETC

Dr. P. DEBRE
DEAN ACADEMICS

Dr. M. KSHIRSAGAR
PRINCIPAL



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Wanadongari, Hingna Road, Nagpur-441110

Department of Electronics & Telecommunication Engineering

TIMETABLE SESSION 2018-19 :- VII Sem ETC(B)



Class Room No.: R-232

w.e.f 13/08/2018

DAY	10.15-11.15 am	11.15-12.15 pm	12.15-1.00pm	1.00-2.00 pm	2.00-3.00 pm	3.00-3.15pm	3.15-4.15pm	4.15-5.15pm
MON	ADSD SJ	DSP&A AW(B)	R E C E S S	DSP P&A/VII SEM-B/ETC/AW/B1 ADSD/VII SEM-B/ETC/SG/B2 TVE/VII SEM-B/ETC/AG/B3 PROJECT/VII SEM-B/ETC/B4		R E C E S S	OC SSK	ADSD SJ
TUE	ADSD SJ	VSP/MEMS AG/PC		DSP P&A/VII SEM-B/ETC/AW/B2 ADSD/VII SEM-B/ETC/SG/B3 ES/VII SEM-ETX/ETC/PDM/B4 PROJECT/VII SEM-B/ETC/B1			OC SSK	DSP&A AW(B)
WED	OC SSK	VSP/MEMS AG/PC		ADSD/SJ/T1 DSPP&A/AW/T2	TVE KT(B)		TVE KT(B)	ADSD/SJ/T2 DSPP&A/AW/T1
THU	DSP&A AW(B)	VSP/MEMS AG/PC		DSP P&A/VII SEM-B/ETC/AW/B3 ADSD/VII SEM-B/ETC/SJ/B4 TVE/VII SEM-B/ETC/AG/B1 PROJECT/VII SEM-B/ETC/B2			TVE KT(B)	VSP/MEMS (TU) AG/PC
FRI	ADSD SJ	TVE KT(B)		DSP P&A/VII SEM-B/ETC/DB/B4 ADSD/VII SEM-B/ETC/SG/B1 TVE/VII SEM-B/ETC/AG/B2 PROJECT/VII SEM-B/ETC/B3			DSP&A AW(B)	OC SSK
SAT	PROJECT			TVE KT(B)TU	TVE KT(B)TU		GATE FORUM/SUDENT INTERACTION/LIBRARY	

Name of Subject	Initials	Name of Faculty
DSP&A DIGITAL SIGNAL PROCESSOR &ARCHITECTURE	AW	Ms. ANAGHA WANKHEDE
	DB	Mr. DHIRAJ BHOJNE
ADSD ADVANCED DIGITAL SYSTEM DESIGN	SJ	Mr. SHASHANT JAYKAR
OC OPTICAL COMMUNICATION	SSK	MRS. SHILPA KATRE
TVE TELEVISION AND VIDEO ENGINEERING	KT	Ms. KOMAL TUMSARE
MEMS MICROELECTRONIMECHANICAL SYSTEMS AND SYSTEM ON CHIP	PC	MS. POONAM CHOUDHARY
VSP VLSI SIGNAL PROCESSING	AG	Ms. APURVA GANAR

LAB	LAB NO
DSP&A	R-227
ADSD	R-228
PROJECT	R-228
TVE	R-216

Class Incharge: Mr. Shashant Jaykar/ Ms. Komal Tumsare

Practical Batches	Tutorial Batches
B1(G5,G7,G8)	B1(Roll No 1-35)
B2(G11,G13,G15)	
B3(G16,G17,G18)	B2(Roll No 36-onwards)
B4(ETX_STUDENTS)	

Ms. POONAM AGARKAR
TIME TABLE INCHARGE

Dr. N. G. NAROLE
HOD ETC

Dr. P. DEBRE
DEAN ACADEMICS

Dr. M. KSHIRSAGAR
PRINCIPAL